

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary) DATE MAILED: <u>March 6, 2002</u>	ATTY. DOCKET NO. 010481	SERIAL NO. Unknown
	APPLICANT Dunworth, et al.	
	FILING DATE March 1, 2002	GROUP Unknown

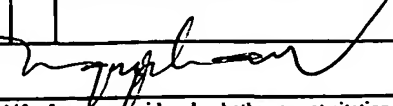
U.S. PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
	A1						
	A2						
	A3						
	A4						
	A5						
	A6						
	A7						
	A8						
	A9						
	A10						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS
	B1						
	B2						
	B3						

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)

807	C1	Wilson, et al., "A CMOS Self-Calibrating Frequency Synthesizer", IEEE Journal of Solid-State Circuits, Vol. 35, No. 10, October 2000, pps 1437 - 1444.
814	C2	Lin, et al., "A 900-MHz 2.5-mA CMOS Frequency Synthesizer with an Automatic SC Tuning Loop", IEEE Journal of Solid-State Circuits, Vol. 36, No. 3, March 2001, pps. 424 - 431.
EXAMINER  DATE CONSIDERED 1/7/05		
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		



STATEMENT BY APPLICANT
(Use several sheets if necessary)

DATE MAILED: August 9, 2004

ATTY. DOCKET NO.
010481

SERIAL NO.
10/092,669

APPLICANT
Dunworth, et al.

FILING DATE
3/6/02

GROUP
~~Unknown~~ 2685

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
814	A1	2 0 0 2 0 0 7 9 9 7 3	6/27/02	Higashi, et al.			11/20/01
82	A2	2 0 0 2 0 0 3 6 5 4 5	3/28/02	Fridi, et al			8/13/01

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
	A3	5 6 0 4 4 6 5	2/18/97	Farabaugh			
	A4	5 1 8 2 5 2 8	01/26/93	Zuta			
	A5						

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AUG 18 2004

FOREIGN PATENT DOCUMENTS

Technology Center 2600

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS
814	B1	1 1 6 8 6 2 7	01/02/02	EPO	Matsushita Electric, etc., et al.		
82	B2	9 9 0 4 4 9 5	01/28/99	WO	Microchip, etc., et al.		
	B3						
	B4						

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)

	C1	
	C2	
	C3	
EXAMINER	DATE CONSIDERED 01/07/05	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Sheet 3 of 3

Sheet 2 of 3

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE				ATTY. DOCKET NO. 010481		SERIAL NO. 10/092,669	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				APPLICANT Dunworth, et al.		GROUP 2682	
DATE MAILED: 11/22/04				FILING DATE March 6, 2002		GROUP 2682	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
D1	A19	6 0 1 8 0 8 1	1/18/02	O'Shaughnessy			
	A30	6 1 3 3 7 9 7	12/17/02	Levins, et al.			
	A21	6 0 6 4 9 4 7	8/16/00	Shin et al.			
	A22	6 1 3 0 8 2 1	1/21/02	Wellins, et al.			
	A24	6 1 3 7 1 2 8	12/1/02	Mauwman			
	A24	6 1 7 2 2 8 0	1/10/01	Lloyd, et al.			
	A25	6 1 7 2 2 8 2	1/16/01	Yaruda			
	A28	6 2 1 1 7 4 7	4/2/01	Trishet et al.			
	A27	6 4 7 3 6 0 7	10/29/02	Shohara et al.			
	A28	6 4 8 8 7 4 8	11/28/02	Manhara et al.			
A32	6 2 4 2 0 4 4	4/1/02	Braun et al.				
FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS
B1							
B2							
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)							
EXAMINER INITIAL	C1	DATE CONSIDERED					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809; Draw line through citation if not in conformance and not amended. Include copy of this form with next communication to applicant.							

Sheet 1 of 3

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE				ATTY. DOCKET NO. 010481		SERIAL NO. 10/092,669	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				APPLICANT Dunworth, et al.		GROUP 2682	
DATE MAILED: 11/22/04				FILING DATE March 6, 2002		GROUP 2682	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
D1	A1	4 0 8 7 7 3 0	2/2/78	Allen, et al.			
	A3	4 3 1 3 2 0 9	1/20/82	Smucker, et al.			
	A4	4 4 3 4 4 8 2	9/12/84	Paylor			
	A4	4 7 2 2 7 0 0	12/20/88	Edred, et al.			
	A6	4 2 1 0 4 7 2	2/20/92	Niwa			
	A6	4 2 4 0 9 2 0	7/10/90	Hedrick			
	A7	5 0 2 3 8 7 2	6/11/91	Caldwell, et al.			
	A8	5 0 7 9 2 2 2	1/7/92	Green, et al.			
	A9	5 1 3 0 0 8 2	9/23/92	Gummert, et al.			
	A10	5 2 3 2 2 6 2	8/2/94	Ballantyne, et al.			
	A11	5 3 8 2 9 2 2	1/17/92	Gorgtsch, et al.			
	A12	5 4 7 2 2 8 4	12/2/92	Levitt, et al.			
	A12	5 4 9 2 7 1 0	2/20/93	Takahara, et al.			
	A13	5 8 2 8 2 9 0	10/27/98	Soyut			
	A12	5 8 2 1 4 8 2	11/2/98	Wilson, et al.			
	A16	5 8 7 0 0 0 1	2/2/92	Caroline, et al.			
	A17	5 8 2 4 3 1 2	7/20/92	Park			
	A18	6 0 1 8 0 8 1	1/18/02	O'Shaughnessy			
FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS
B1	2 2 6 8 0 7 8		10/15/93	JP	Nara Osamu		
B2	2 0 0 1 2 8 0 8 6 7		8/24/91	JP	Kobayashi Hirosaki		
B3	0 4 2 3 9 4 1		9/19/90	EPO	Marconi Instruments Limited		
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)							
EXAMINER INITIAL	C1	DATE CONSIDERED					
C2		"A Low-Noise Multiple Output Phase Locked Loop Synthesizer for Radar Applications" European Frequency and Time Forum (2002)					
C2		"Design of an Integrated CMOS PLL Frequency Synthesizer" IEEE					
DATE CONSIDERED 01/07/05							